

Review of ECE 3040

Chapter 3 - Diodes and Diode Circuits:

Diode

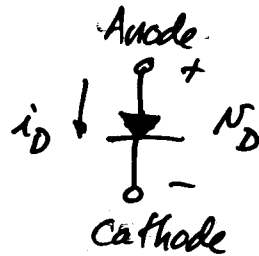
Two-terminal device

I-V characteristics

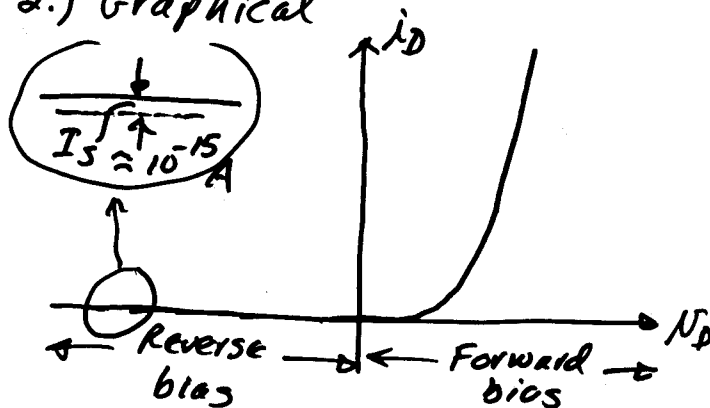
1.) Mathematical

$$i_D = I_S \left[\exp\left(\frac{V_D}{V_T}\right) - 1 \right] \quad \text{where } V_T = \frac{kT}{q}$$

$$\left\{ \begin{array}{l} I_S = K T^3 \exp\left(-\frac{V_{GO}}{V_T}\right) \\ \text{Bandgap voltage} \\ \approx 1.2 \text{ V} \end{array} \right.$$

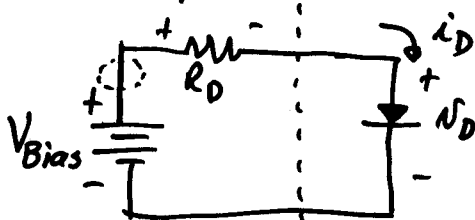


2.) Graphical

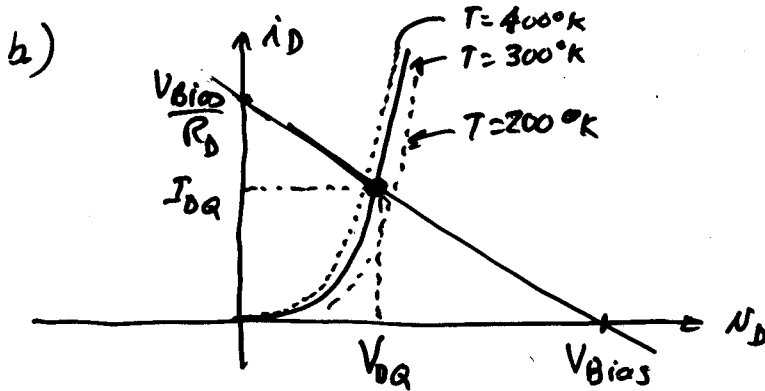


Application of the Diode Model -

Example:



$$\begin{aligned} \text{a.) } V_{\text{bias}} &= i_D R_D + V_D \\ (i_D \approx I_S \exp\left(\frac{V_D}{V_T}\right)) & \quad + V_D > 0 \\ i_D &= \frac{V_{\text{bias}} - V_D}{R_D} \approx \frac{V_{\text{bias}}}{R_D} \\ & \text{where } V_D \ll V_D \\ & \text{or iterate} \end{aligned}$$



Temperature Characteristics

Dependence of i_D & V_D as a function of temperature.

Forward bias: 0.7 0.075 1.2

$$\frac{dV_D}{dT} = \frac{V_D + 3V_T + V_{60}}{T - 300K} \approx -2mV/^{\circ}C$$

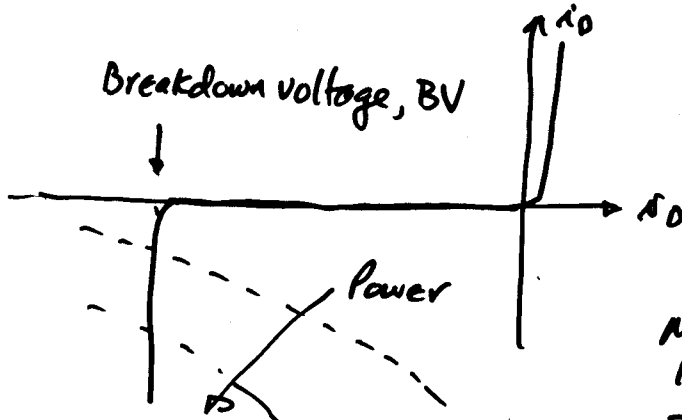
$$\frac{\partial i_D}{\partial T} > 0$$

Reverse bias:

$$i_D = -I_S \rightarrow \frac{1}{I_S} \frac{\partial I_S}{\partial T} \quad (\text{ppm}/^{\circ}C)$$

I_S approx doubles for every $10^{\circ}C$ increase $\left\{ \frac{3}{T} + \frac{V_{60}}{TV_T} \right\}$

Breakdown Characteristics



Electric field in the depletion region reaches a critical level and the diode goes into a high conduction state.

Mechanisms

- 1.) Avalanche - higher voltages
- 2.) Zener - low voltage & high doping