

LECTURE 150 – SIMPLE BJT OP AMPS

(READING: Text-GHLM – 425-434, 453-454, AH – 249-253)

INTRODUCTION

The objective of this presentation is:

- 1.) Illustrate the analysis of BJT op amps
- 2.) Prepare for the design of BJT op amps

Outline

- Simple BJT Op Amps
 - Two-stage
 - Folded-cascode
- Summary

SIMPLE TWO-STAGE BJT OP AMPS

BJT Two-Stage Op Amp

Circuit:

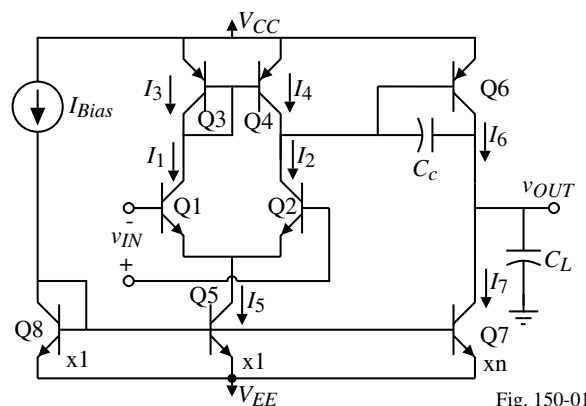


Fig. 150-01

DC Conditions:

$$I_5 = I_{bias}, \quad I_1 = I_2 = 0.5I_5 = 0.5I_{bias}, \quad I_7 = I_6 = nI_{bias}$$

$$V_{icm}(\max) = V_{CC} - V_{EB3} - V_{CE1}(\text{sat}) + V_{BE1}$$

$$V_{icm}(\min) = V_{EE} + V_{CE5}(\text{sat}) + V_{BE1}$$

$$V_{out}(\max) = V_{CC} - V_{EC6}(\text{sat})$$

$$V_{out}(\min) = V_{EE} + V_{CE7}(\text{sat})$$

Notice that the output stage is class A $\Rightarrow I_{sink} = I_7$ and $I_{source} = \beta F I_5 - I_7$

Two-Stage BJT Op Amp - Continued

Small Signal Performance:

Assuming differential mode operation, we can write the small-signal model as,

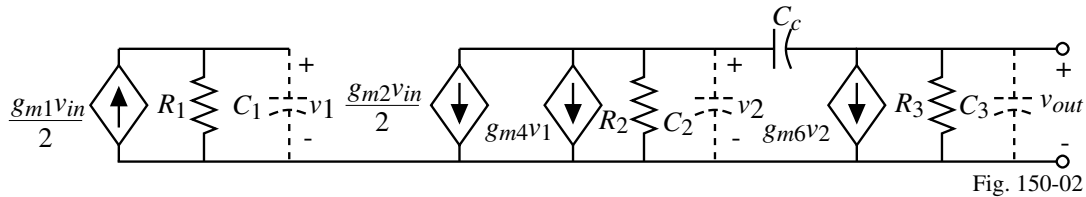


Fig. 150-02

where,

$$R_1 = \frac{1}{g_{m3}} \parallel r_{\pi 3} \parallel r_{\pi 4} \parallel r_{o3} \approx \frac{1}{g_{m3}} \quad R_2 = r_{\pi 6} \parallel r_{o2} \parallel r_{o4} \approx r_{\pi 6} \quad \text{and} \quad R_3 = r_{o6} \parallel r_{o7}$$

$$C_1 = C_{\pi 3} + C_{\pi 4} + C_{cs1} + C_{cs3} \quad C_2 = C_{\pi 6} + C_{cs2} + C_{cs4} \quad \text{and} \quad C_3 = C_L + C_{cs6} + C_{cs7}$$

Note that we have ignored the base-collector capacitors, C_{μ} , except for M6, which is called C_c .

Assuming the pole due to C_1 is much greater than the poles due to C_2 and C_3 gives

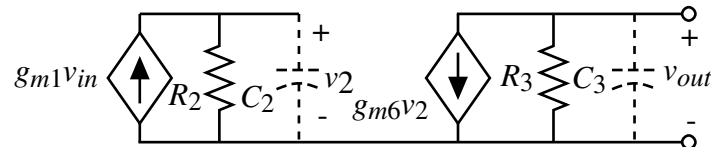


Fig. 150-03

Two-Stage BJT Op Amp - Continued

Summary of the small signal performance:

Midband performance-

$$A_o = g_{m1} g_{mII} R_I R_{II} \approx g_{m1} g_{m6} r_{\pi 6} (r_{o6} \parallel r_{o7}) = g_{m1} \beta_F r_{\pi 6} (r_{o6} \parallel r_{o7}), \quad R_{out} = r_{o6} \parallel r_{o7}, \quad R_{in} = 2r_{\pi 1}$$

Roots-

$$\text{Zero} = \frac{g_{mII}}{C_c} = \frac{g_{m6}}{C_c}$$

$$\text{Poles at } p_1 \approx \frac{-1}{g_{mII} R_I R_{II} C_c} = \frac{-1}{g_{m6} r_{\pi 6} (r_{o6} \parallel r_{o7}) C_c} = \frac{-g_{m1}}{A_o C_c} \quad \text{and} \quad p_2 \approx \frac{-g_{mII}}{C_{II}} \approx \frac{-g_{m6}}{C_L}$$

Assume that $\beta_F = 100$, $g_{m1} = 1\text{mS}$, $g_{m6} = 10\text{mS}$, $r_{o6} = r_{o7} = 0.5\text{M}\Omega$, $C_c = 5\text{pF}$ and $C_L = 10\text{pF}$:

$$A_o = (1\text{mS})(100)(250\text{k}\Omega) = 25,000\text{V/V}, \quad R_{in} = 2(\beta_F/g_{m1}) = 2(100\text{k}\Omega) = 200\text{k}\Omega, \quad R_{out} = 250\text{k}\Omega$$

$$\text{Zero} = \frac{10\text{mS}}{5\text{pF}} = 2 \times 10^9 \text{ rads/sec}$$

$$\text{or } 318.3\text{MHz},$$

$$p_1 = \frac{-1\text{mS}}{(25,000)5\text{pF}} = \frac{-2 \times 10^8}{25,000}$$

$$= -8000 \text{ rads/sec or } 1273\text{Hz},$$

$$\text{and } p_2 = -10\text{mS}/10\text{pF} = -10^9 \text{ rads/sec or } 159.15\text{MHz}$$



Fig. 150-04

Slew Rate of the Two-Stage BJT Op Amp

Remember that slew rate occurs when currents flowing in a capacitor become limited and is given as

$$I_{lim} = C \frac{dv_C}{dt} \text{ where } v_C \text{ is the voltage across the capacitor } C.$$

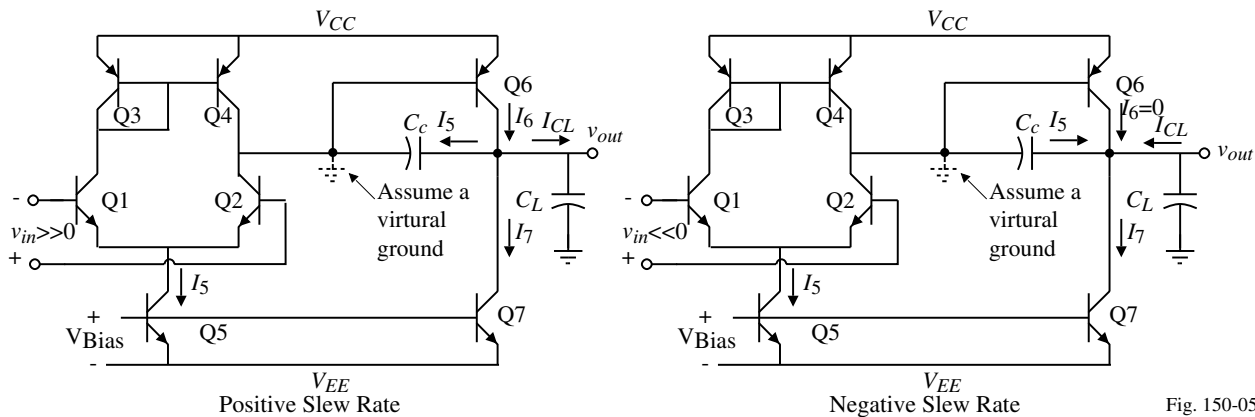


Fig. 150-05

$$SR^+ = \min \left[\frac{I_5}{C_c}, \frac{I_6 - I_5 - I_7}{C_L} \right] = \frac{I_5}{C_c} \text{ because } I_6 \gg I_5 \quad SR^- = \min \left[\frac{I_5}{C_c}, \frac{I_7 - I_5}{C_L} \right] = \frac{I_5}{C_c} \text{ if } I_7 \gg I_5.$$

Therefore, if C_L is not too large and if I_7 is significantly greater than I_5 , then the slew rate of the two-stage op amp should be,

$$SR = \frac{I_5}{C_c}$$

Folded-Cascode BJT Op Amp

Circuit

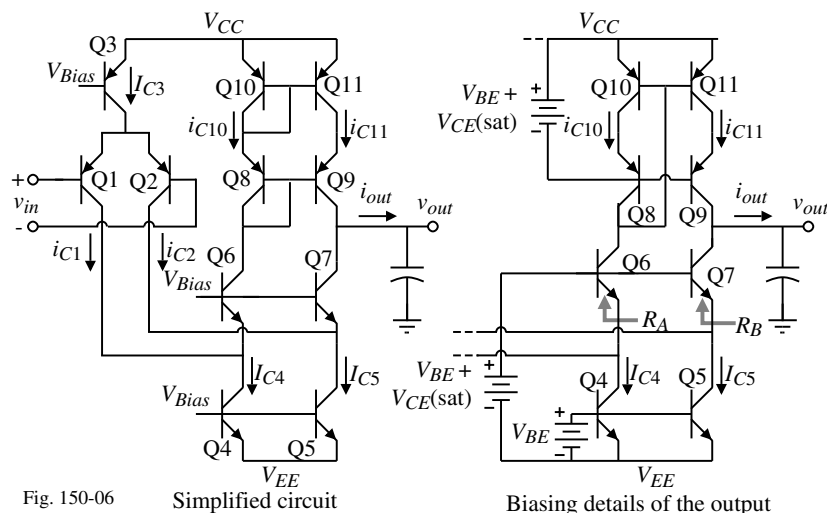


Fig. 150-06

Simplified circuit

Biasing details of the output

DC Conditions:

$$I_3 = I_{bias}, I_1 = I_2 = 0.5I_5 = 0.5I_{bias}, I_4 = I_5 = kI_{bias}, I_{10} = I_{11} = kI_{bias} - 0.5I_{bias} \quad (k > 1)$$

$$V_{icm(max)} = V_{CC} - V_{CE3(sat)} + V_{EB1} \quad V_{icm(min)} = V_{EE} + V_{CE4(sat)} + V_{EC1(sat)} - V_{BE1}$$

$$V_{out(max)} = V_{CC} - V_{EC9(sat)} - V_{EC11(sat)} \quad V_{out(min)} = V_{EE} + V_{CE5(sat)} + V_{CE7(sat)}$$

Notice that the output stage is push-pull $\Rightarrow I_{sink}$ and I_{source} are limited by the base current.

Folded-Cascode BJT Op Amp - Continued

Small-Signal Analysis:

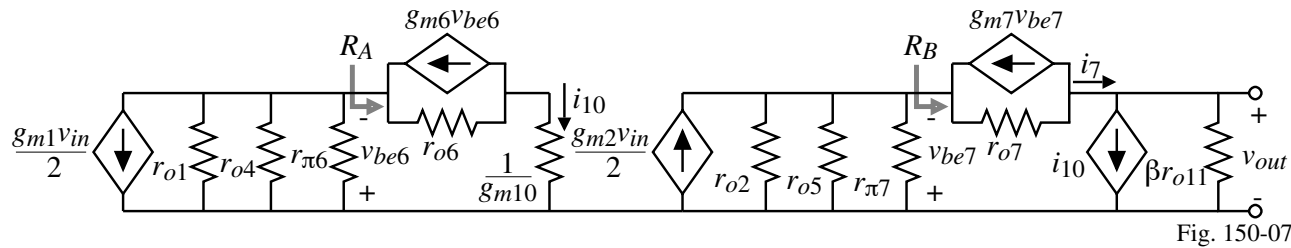


Fig. 150-07

where $R_A \approx 1/g_{m6}$ and $R_B \approx \frac{r_{o7} + \beta p r_{o11}/2}{1 + g_{m7} r_{o7}} \approx \frac{r_{\pi 7}}{2}$ if $r_{o7} \approx r_{o11}$

$$i_{10} \approx \frac{-g_{m1} r_{\pi 6} v_{in}}{2(r_{\pi 6} + R_A)} \approx \frac{-g_{m1} v_{in}}{2} \quad i_7 \approx \frac{g_{m2} r_{\pi 7} v_{in}}{2(r_{\pi 7} + R_B)} \approx \frac{g_{m2} r_{\pi 7} v_{in}}{2(r_{\pi 7} + 0.5 r_{\pi 7})} = \frac{g_{m2} v_{in}}{3}$$

$$\therefore v_{out} = (i_7 - i_{10}) R_{out} v_{in} = \frac{5}{6} (g_{m1} R_{out}) v_{in} \quad \text{if } g_{m1} = g_{m2} \Rightarrow \frac{v_{out}}{v_{in}} = \frac{5}{6} (g_{m1} R_{out})$$

$$R_{out} = \beta p r_{o11} \parallel [\beta_N (r_{o5} \parallel r_{o2})] \quad \text{and} \quad R_{in} = 2 r_{\pi 1}$$

Assume that $\beta_{FN} = 100$, $\beta_{FP} = 50$, $g_{m1} = g_{m2} = 1 \text{ mS}$, $r_{oN} = 1 \text{ M}\Omega$, and $r_{oP} = 0.5 \text{ M}\Omega$:

$$\frac{v_{out}}{v_{in}} = 14,285 \text{ V/V} \quad R_{out} = 14.285 \text{ M}\Omega \quad \text{and} \quad R_{in} = 100 \text{ k}\Omega$$

Folded-Cascode BJT Op Amp - Continued

Frequency response includes only 1 dominant pole at the output (self-compensation),

$$p_1 = \frac{-1}{R_{out} C_L}$$

There are other poles but we shall assume that they are less than GB

If $C_L = 25 \text{ pF}$, then $|p_1| = 2800 \text{ rads/sec. or } 446 \text{ Hz} \Rightarrow GB = 6.371 \text{ MHz}$

Checking some of the nondominant poles gives:

$$|p_A| = \frac{1}{R_A C_A} = \frac{g_{m6}}{C_A} \Rightarrow 159 \text{ MHz if } C_A = 1 \text{ pf}$$

(the capacitance to ac ground at the emitter of Q6)

$$|p_B| = \frac{1}{R_B C_B} = \frac{2}{r_{\pi 7} C_B} \Rightarrow 6.37 \text{ MHz if } C_B = 1 \text{ pf} \quad \text{(the capacitance to ac ground at the}$$

emitter of Q7)

This indicates that for small capacitive loads, this op amp will suffer from higher poles with respect to phase margin. Capacitive loads greater than 25 pF , will have better stability (and less GB).

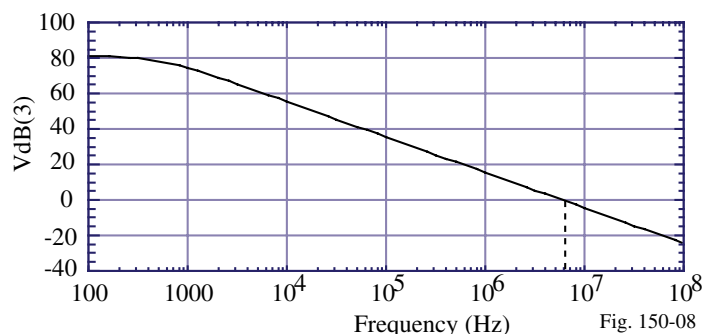


Fig. 150-08

SUMMARY

- Two stage op amp gives reasonably robust performance as an “on-chip” op amp
- DC balance conditions insure proper mirroring and all transistors in saturation
- Slew rate of the two-stage op amp is I_5/C_c
- Folded cascode op amp offers wider input common voltage range
- Folded cascode op amp is a self-compensated op amp because the dominant pole at the output and proportional to the load capacitor