

## LECTURE 250 – INTRODUCTION TO FEEDBACK CONCEPTS (READING: GHLM – 553-563)

### Objective

The objective of this presentation is:

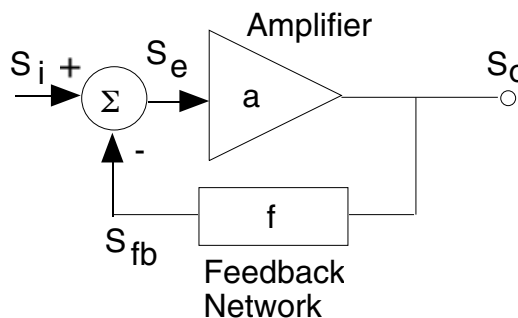
1.) Introduce the background and basic concepts of negative feedback

### Outline

- Influence of negative feedback
- Feedback configurations with ideal source/load
- Examples
- Summary

### Ideal Feedback Equation

Ideal Negative Feedback Configuration:



$$S_e = S_i - S_{fb} = S_i - fS_o$$

$$S_o = aS_e = aS_i - afS_o$$

$$A = \frac{S_o}{S_i} = \frac{a}{1 + af} \Rightarrow \text{Closed Loop Gain} \quad T = af \Rightarrow \text{Loop Gain}^\dagger$$

$$A = \frac{S_o}{S_i} = \frac{a}{1 + T} \quad \text{for } T \gg 1, \quad A \rightarrow \frac{1}{f}$$

$$\text{Feedback signal/Input signal} = \frac{S_{fb}}{S_i} = \frac{T}{1 + T}$$

$$\text{Error signal/Input signal} = \frac{S_e}{S_i} = \frac{1}{1 + T}$$

<sup>†</sup> Some authors (Allen and Holberg) define the loop gain as  $-af$ .

## Gain Sensitivity

Sensitivity of the closed loop gain with respect to the open loop gain:

$$\frac{dA}{da} = \frac{1 + af - af}{(1 + af)^2} = \frac{1}{(1 + T)^2} = \frac{1}{a} \frac{a}{(1 + T)^2} = \frac{A}{a} \frac{1}{1 + T}$$

$$\frac{dA}{A} = \frac{1}{(1 + T)} \frac{da}{a}$$

Example:

An amplifier with a gain of 10 has a gain variation of 3.3%. Use these amplifiers to design an amplifier with a gain of 10 and a gain variation  $\leq 0.1\%$ .

Solution:

We must cascade several amplifiers together and feedback around the cascade. With feedback,  $dA/A = 0.001$ . For three cascaded stages,

$$a = 1000 \quad \text{and} \quad \frac{da}{a} = \frac{[10(1+0.033)]^3 - 1000}{1000} = 0.1023$$

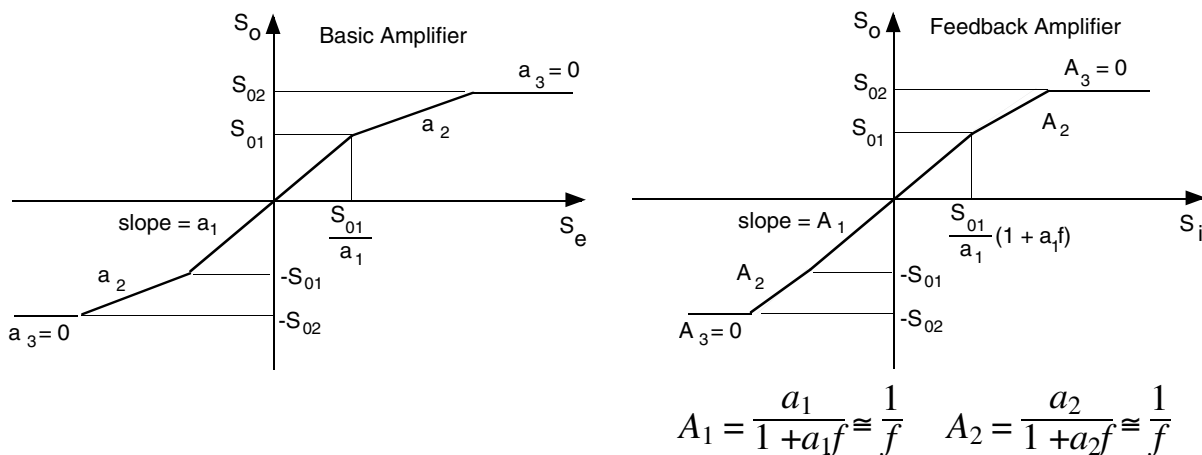
$$\frac{dA}{A} = \frac{1}{(1 + T)} \frac{da}{a} = \frac{0.1023}{1 + T} = 0.001 \Rightarrow T = 101.$$

$$A = \frac{1000}{1 + 101} = 9.80 \quad a = 1000 \quad f = \frac{101}{1000} = 0.101$$

Gain is close to 10 but not exactly. However, the tolerance will be 0.1%

## Effect of Negative Feedback on Nonlinear Distortion

The major cause of nonlinear distortion in amplifiers is due to changes of gain with signal level.



Key points:

1. Three gain regions exist for each case
2. Horizontal scale compression for feedback amplifier
3. No distortion improvement for hard saturation cases
4. Distortion improvement with feedback  $\approx (1 + T)$

### Demonstration 1 – Reduction of Nonlinearity by Negative Feedback

Consider the following circuit for this demonstration.

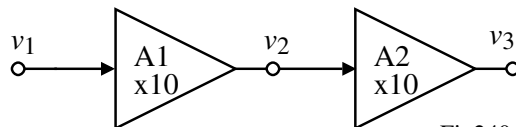
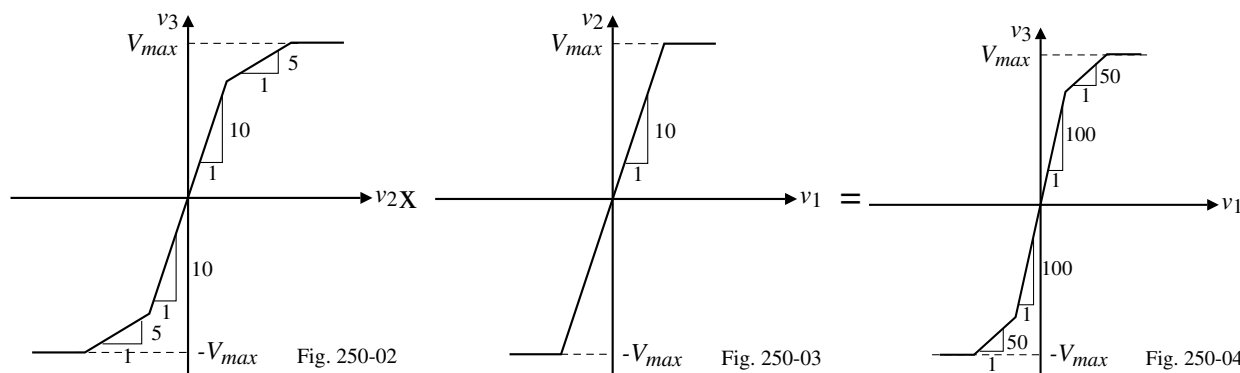


Fig240-01

Assume that the amplifiers have the following voltage transfer functions.



### Demonstration 1 - Continued

Now let us apply feedback around the second stage resulting in a gain of x2 and increase the gain of the first stage from x10 to x50.

To find the value of  $f$ , we can solve the following,

$$\frac{v_3}{v_2} = \frac{A_2}{1+A_2f} = 2 \Rightarrow f = 0.4$$

The resulting transfer function is,

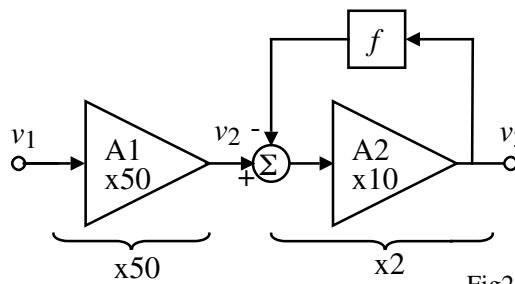


Fig240-05

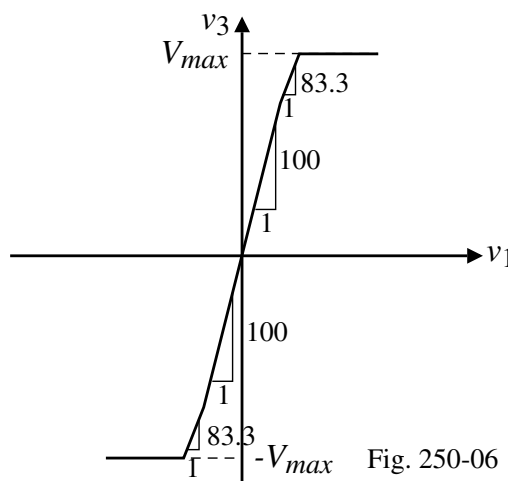


Fig. 250-06

## Demonstration 1 – Continued

The demo circuit:

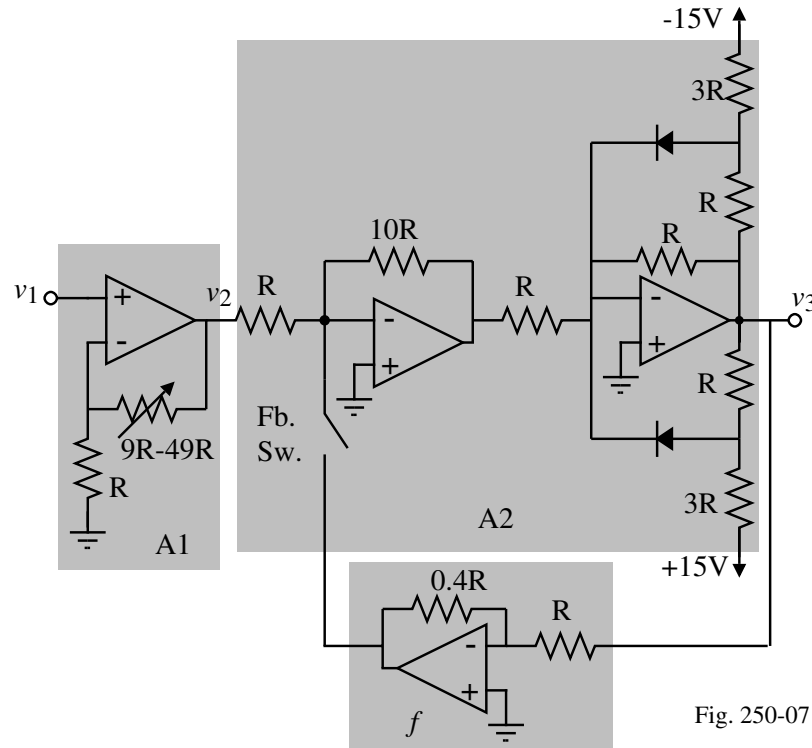
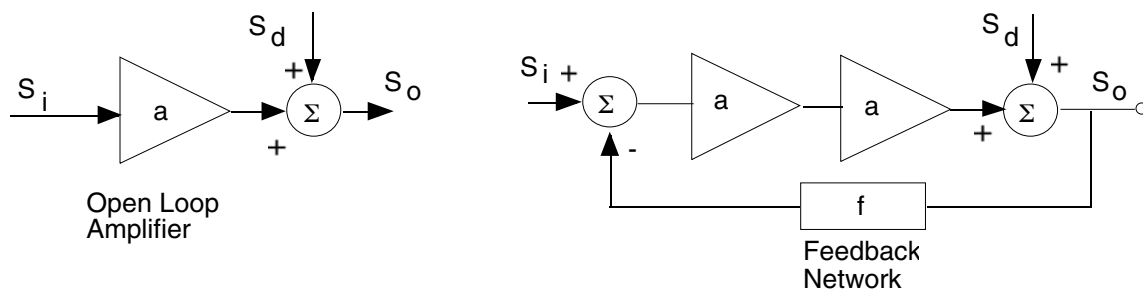


Fig. 250-07

## Effects of Negative Feedback on Signal Distortion

Consider the output stage of an amplifier where distortion occurs because this stage is being driven very hard and a distortion signal  $S_d$  is introduced. Examine the two cases of with and without feedback.



$$S_o = aS_i + S_d$$

$$S_o = \frac{a^2 S_i}{1 + a^2 f} + \frac{S_d}{1 + a^2 f}$$

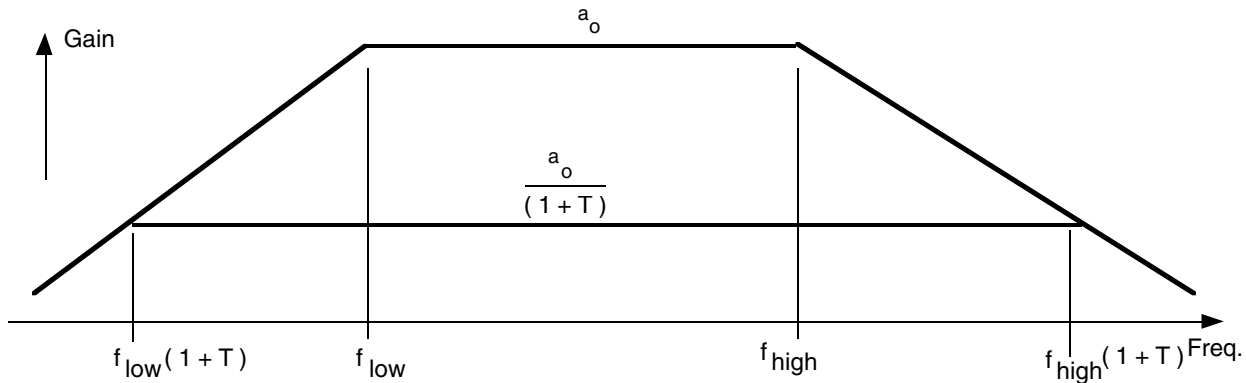
$$\text{Now choose } f = \frac{a-1}{a^2} \rightarrow S_o = aS_i + \frac{S_d}{a}$$

Key points:

1. Desired output signal is the same in both cases
2. With feedback, distortion is reduced by gain “a”
3.  $T = a^2 f = a - 1$
4. Improved performance with inferior amplifier (if used in large quantities – sometimes more can be better)

### Influence of Negative Feedback on Frequency Response

When negative feedback is used with an amplifier having a single dominant low and a single dominant high frequency pole, gain and bandwidth can be traded evenly as shown.

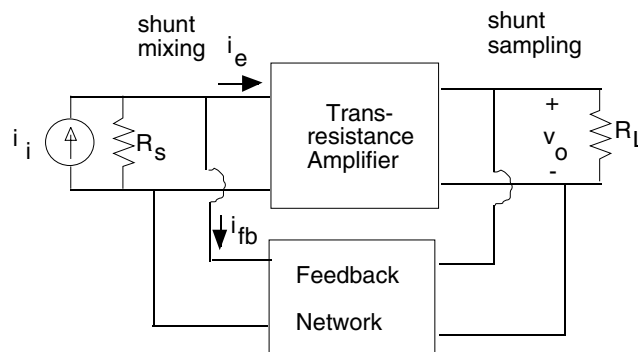
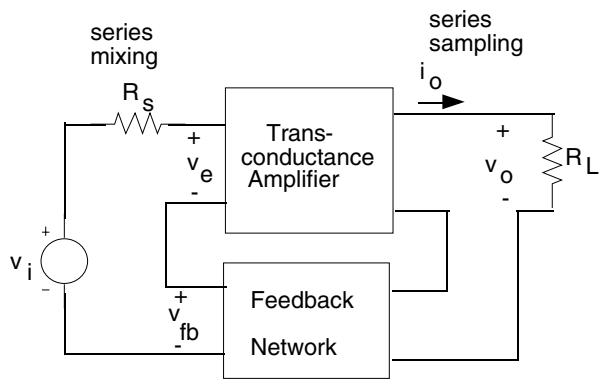
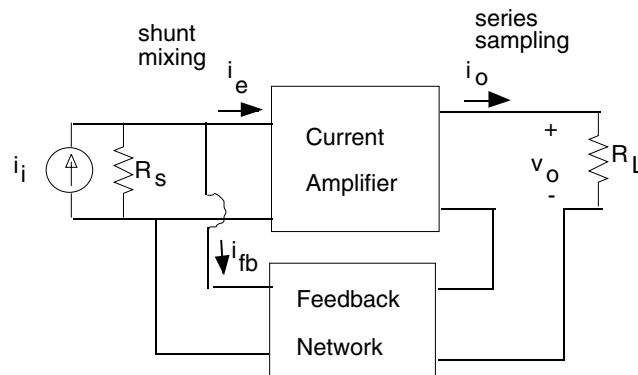
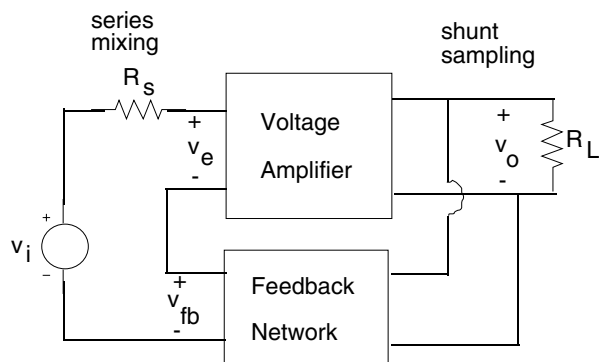


### Four Basic Amplifiers

Parameter	Amplifier Type			
	Voltage	Transconductance	Transresistance	Current
Small-Signal Model				
Ideal Forward Gain	$v_o = A_v v_e$	$i_o = G_m v_e$	$v_o = R_m i_e$	$i_o = A_i i_e$
Ideal $R_i$	$\rightarrow \infty \Omega$	$\rightarrow \infty \Omega$	$\rightarrow 0 \Omega$	$\rightarrow 0 \Omega$
Ideal $R_o$	$\rightarrow 0 \Omega$	$\rightarrow \infty \Omega$	$\rightarrow 0 \Omega$	$\rightarrow \infty \Omega$
$S_i, S_{fb}$ and $S_e$	Voltage	Voltage	Current	Current
$S_o$ Variable	Voltage	Current	Voltage	Current
SS Model with Source and Load				
Ideal source, $R_S$	$R_S=0$ or $R_S \ll R_i$	$R_S=0$ or $R_S \ll R_i$	$R_S=\infty$ or $R_S \gg R_i$	$R_S=\infty$ or $R_S \gg R_i$
Ideal load, $R_L$	$R_L=\infty$ or $R_L \gg R_o$	$R_L=0$ or $R_L \ll R_o$	$R_L=\infty$ or $R_L \gg R_o$	$R_L=0$ or $R_L \ll R_o$
Overall Forward Gain	$\frac{R_i R_L A_v}{(R_S + R_i)(R_L + R_o)}$	$\frac{R_i R_o G_m}{(R_S + R_i)(R_L + R_o)}$	$\frac{R_S R_L R_m}{(R_S + R_i)(R_L + R_o)}$	$\frac{R_S R_L A_i}{(R_S + R_i)(R_L + R_o)}$

### FEEDBACK WITH IDEAL SOURCE AND LOAD

### Four Basic Feedback Topologies



### Series-Shunt Feedback Amplifier

Series-shunt configuration:

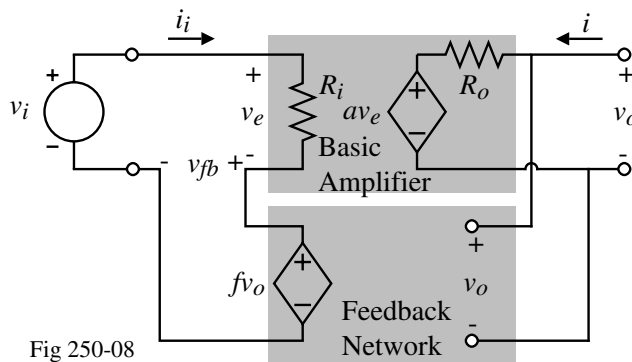


Fig 250-08

Find the equivalent voltage gain, input resistance ( $R_{if}$ ), and output resistance ( $R_{of}$ ).

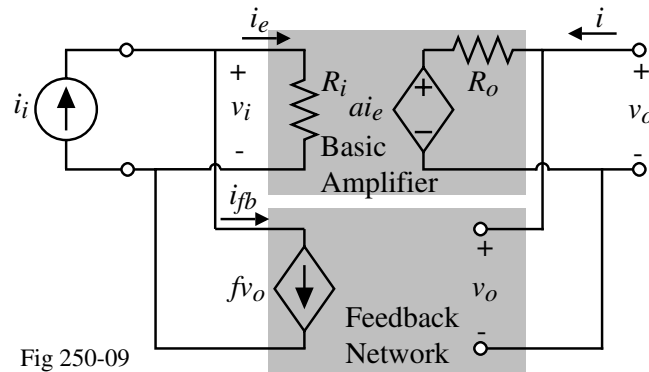
$$v_o = av_e = a(v_i - v_{fb}) = av_i - av_{fb} = av_i - afv_o \rightarrow v_o(1+af) = av_i \rightarrow \frac{v_o}{v_i} = \frac{a}{1+af}$$

$$R_{if} = \frac{v_i}{i_i} = \frac{v_i}{v_e} R_i = R_i \frac{v_e + v_{fb}}{v_e} = R_i \frac{v_e + fv_o}{v_e} = R_i \frac{v_e + afv_e}{v_e} = R_i(1+af) = R_i(1+T)$$

$$R_{of} = \left. \frac{v_o}{i} \right|_{v_i=0} = \frac{v_o - av_e}{R_o} = \frac{v_o - a(-v_{fb})}{R_o} = \frac{v_o + afv_o}{R_o} \Rightarrow R_{of} = \frac{R_o}{1+af} = \frac{R_o}{1+T}$$

## Shunt-Shunt Feedback Amplifier

Shunt-shunt feedback configuration:



$$v_o = a i_e = a(i_i - i_{fb}) = a i_i - a i_{fb} = a v_i - a f v_o \quad \rightarrow \quad v_o(1 + a f) = a i_i \quad \rightarrow \quad \frac{i_o}{i_i} = \frac{a}{1 + a f}$$

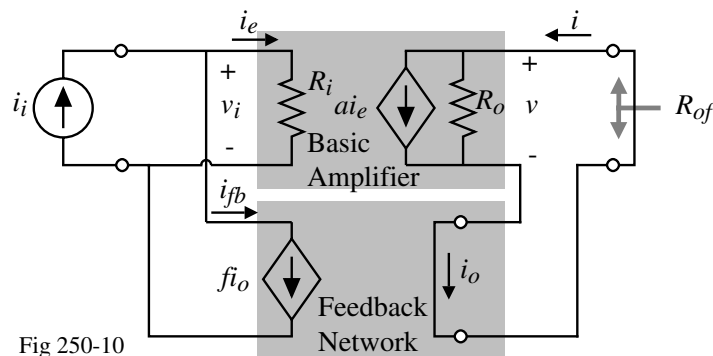
$$R_{if} = \frac{v_i}{i_i} = \frac{i_e R_i}{i_e + i_{fb}} = \frac{i_e R_i}{i_e + f i_o} = \frac{i_e R_i}{i_e + a f i_e} = \frac{R_i}{1 + a f} = \frac{R_i}{1 + T}$$

$$R_{of} = \left. \frac{v_o}{i} \right|_{i_i=0} \quad v_o = R_o i + a i_e = R_o i + a(-i_{fb}) = R_o i - a f v_o \quad \rightarrow \quad v_o(1 + a f) = R_o i$$

$$\therefore R_{of} = \frac{v_o}{i} = \frac{R_o}{1 + a f} = \frac{R_o}{1 + T}$$

## Shunt-Series Feedback Amplifier

Shunt-series feedback configuration:



$$i_o = a i_e = a(i_i - i_{fb}) = a i_i - a i_{fb} = a i_i - a f i_o \quad \rightarrow \quad i_o(1 + a f) = a i_i \quad \rightarrow \quad \frac{i_o}{i_i} = \frac{a}{1 + a f}$$

$$R_{if} = \frac{v_i}{i_i} = \frac{i_e R_i}{i_e + i_{fb}} = \frac{i_e R_i}{i_e + f v_o} = \frac{i_e R_i}{i_e + a f i_e} = \frac{R_i}{1 + a f} = \frac{R_i}{1 + T}$$

$$R_{of} = \left. \frac{v}{i_o} \right|_{i_i=0} \quad R_{of} = \frac{v}{i_o} = \frac{R_o(i_o - a i_e)}{i_o} = \frac{R_o(i_o + a f i_o)}{i_o} = R_o(1 + a f)$$

### Series-Series Feedback Amplifier

Series-series feedback configuration:

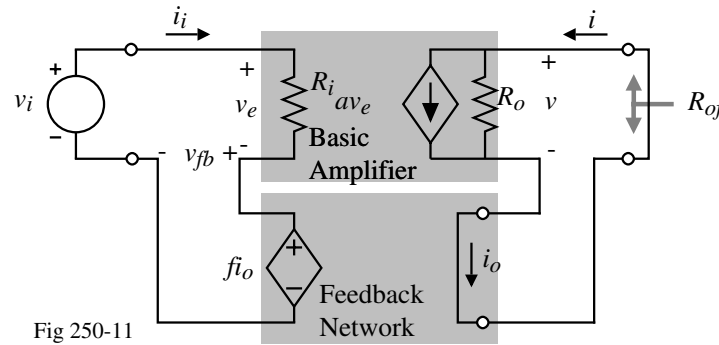


Fig 250-11

$$i_o = av_e = a(v_i - v_{fb}) = av_i - av_{fb} = av_i - afi_o \rightarrow i_o(1+af) = av_i \rightarrow \frac{i_o}{v_i} = \frac{a}{1+af}$$

$$R_{if} = \frac{v_i}{i_i} = \frac{v_i}{v_e} R_i = R_i \frac{v_e + v_{fb}}{v_e} = R_i \frac{v_e + fi_o}{v_e} = R_i \frac{v_e + afv_e}{v_e} = R_i(1+af) = R_i(1+T)$$

$$R_{of} = \left. \frac{v}{i_o} \right|_{i_o=0} = \frac{v}{i_o} = \frac{R_o(i_o - av_e)}{i_o} = \frac{R_o(i_o + afi_o)}{i_o} = R_o(1+af)$$

## SUMMARY

### Four Basic Feedback Topologies (Ideal Source/Load)

Parameter	Amplifier Type			
	Voltage	Transconductance	Transresistance	Current
Feedback Topology	Series-shunt	Series-series	Shunt-shunt	Shunt-series
$S_i, S_{fb}$ and $S_e$	Voltage	Voltage	Current	Current
$S_o$ Variable	Voltage	Current	Voltage	Current
Closed Loop Gain	$A_{vf} = \frac{A_v}{1+afA_v}$	$G_{mf} = \frac{G_m}{1+afG_m}$	$R_{mf} = \frac{R_m}{1+afR_m}$	$A_{if} = \frac{A_i}{1+afA_i}$
Closed Loop Input Resistance	$R_{if} = R_i(1+afA_v)$	$R_{if} = R_i(1+afG_m)$	$R_{if} = \frac{R_i}{1+afR_m}$	$R_{if} = \frac{R_i}{1+afA_i}$
Closed Loop Output Resistance	$R_{of} = \frac{R_i}{1+afA_v}$	$R_{of} = R_o(1+afG_m)$	$R_{of} = \frac{R_i}{1+afR_m}$	$R_{of} = R_o(1+afA_i)$